Gp 2302 #5

PATENT

Docket No. 12172-50 / A

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SEP 1 3 1994   9/13/94

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:)
)   Examiner: V. Darbe
Howard G. Sachs, *et al.*   )
)   Art Unit: 2302
Serial No. 08/147,800   )
)
Filed: November 5, 1993   )
)
For: Software Scheduled   )   **AMENDMENT**
      Superscaler Computer   )
      Architecture   )
)   San Francisco, CA 94105

Commissioner of Patents and Trademarks
Washington, D. C. 20231

Sir:

        Responsive to the Office Action mailed April 21, 1994, please amend the above-identified patent application as follows.

In the Claims

        All of the claims now pending in the application are reproduced below for the Examiner's convenience whether they have been amended or not. Please amend the claims as indicated.

1   sub   1.   (Amended) A computing system for executing a
2   cl   group[s] of individual instructions in parallel, each of the
3   individual instructions in a group being executed by a
4   a   different [by] processing pipeline[s], the system
5   comprising: